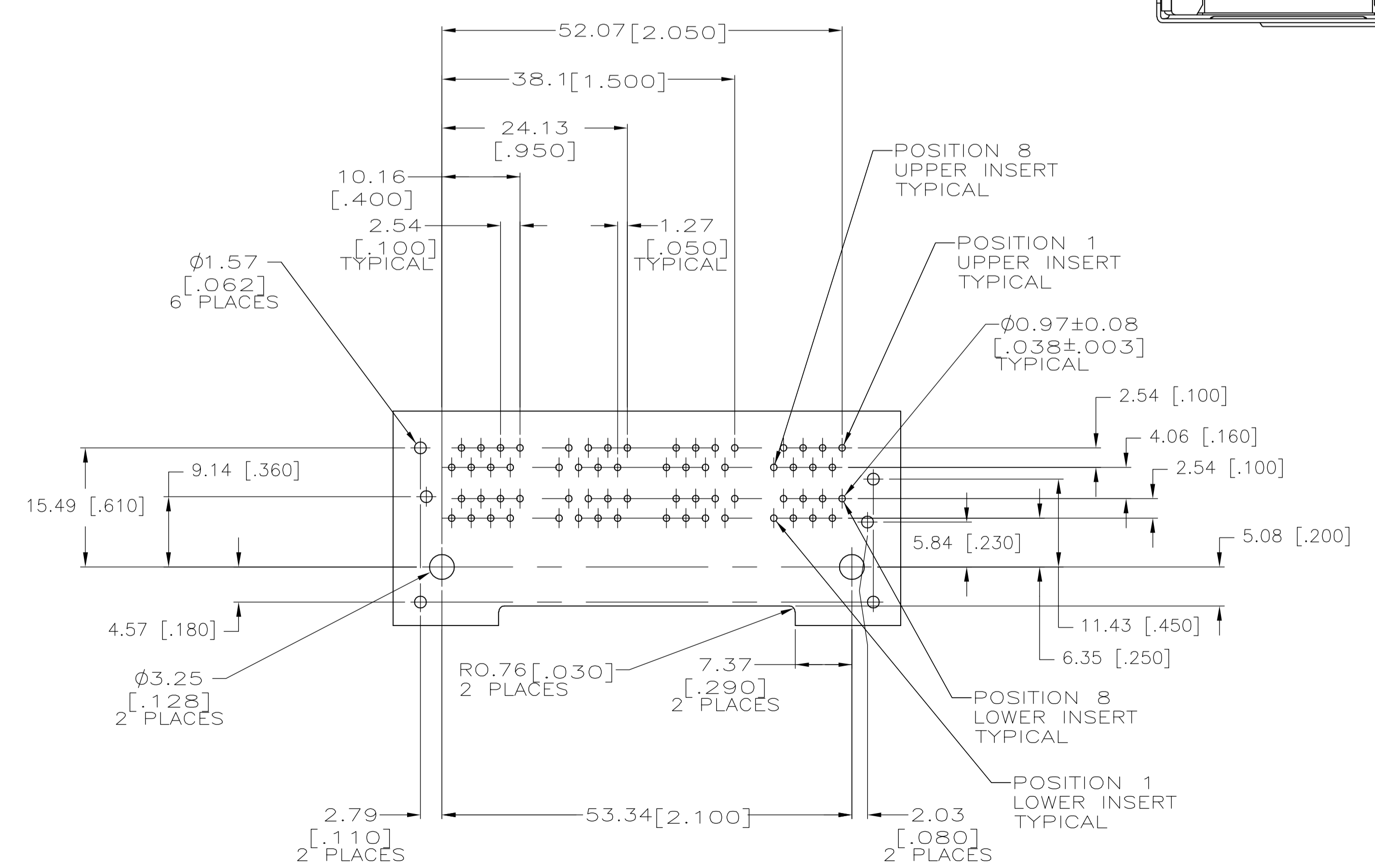
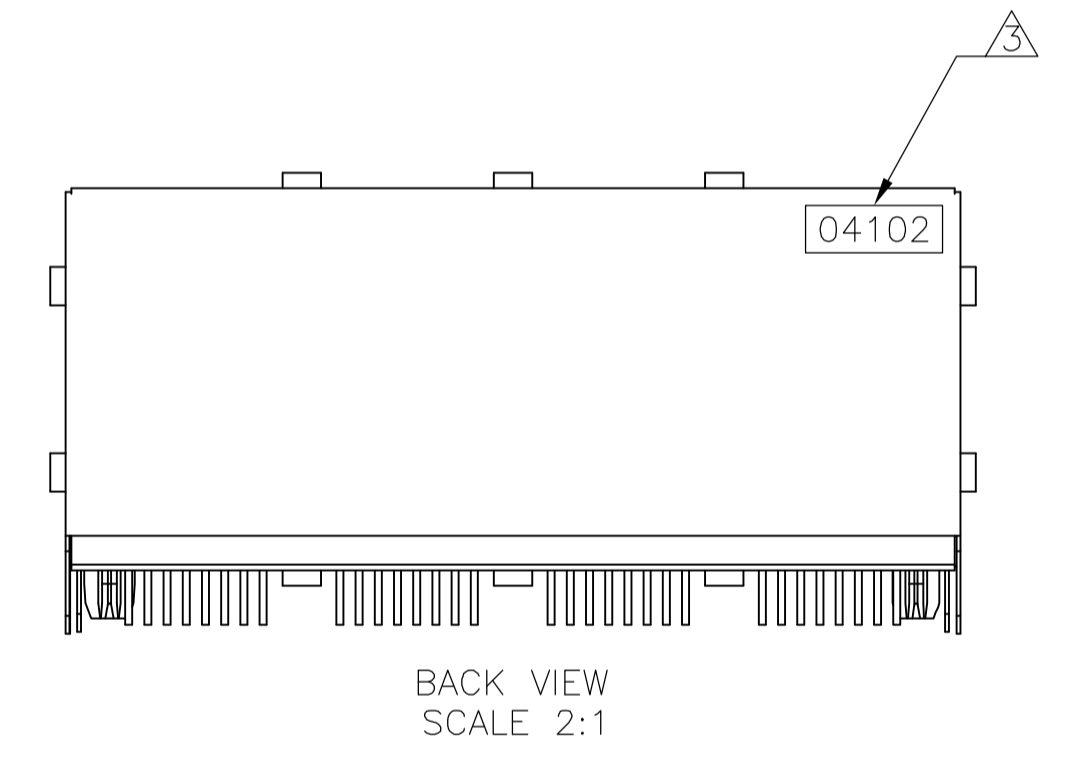
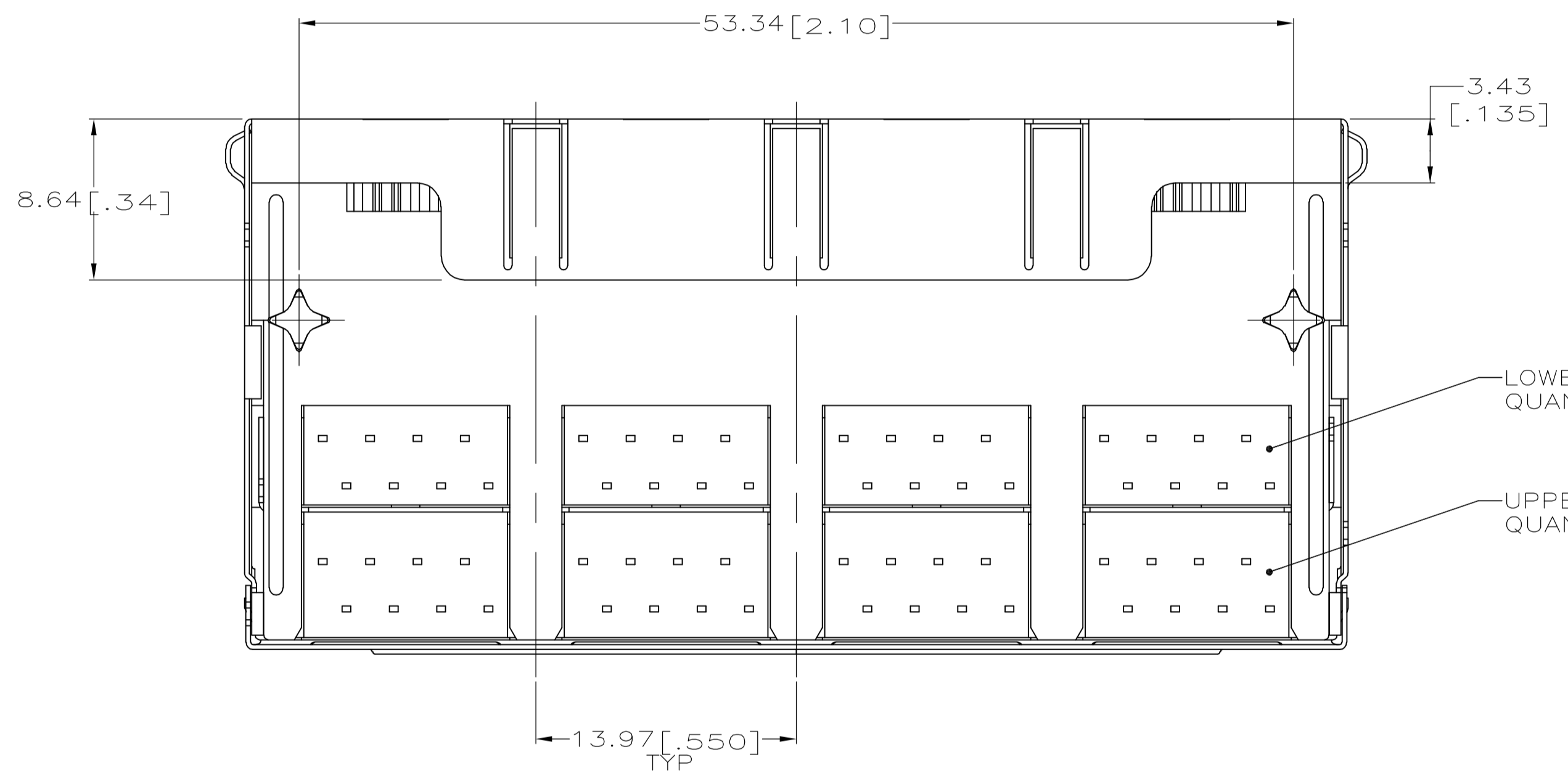
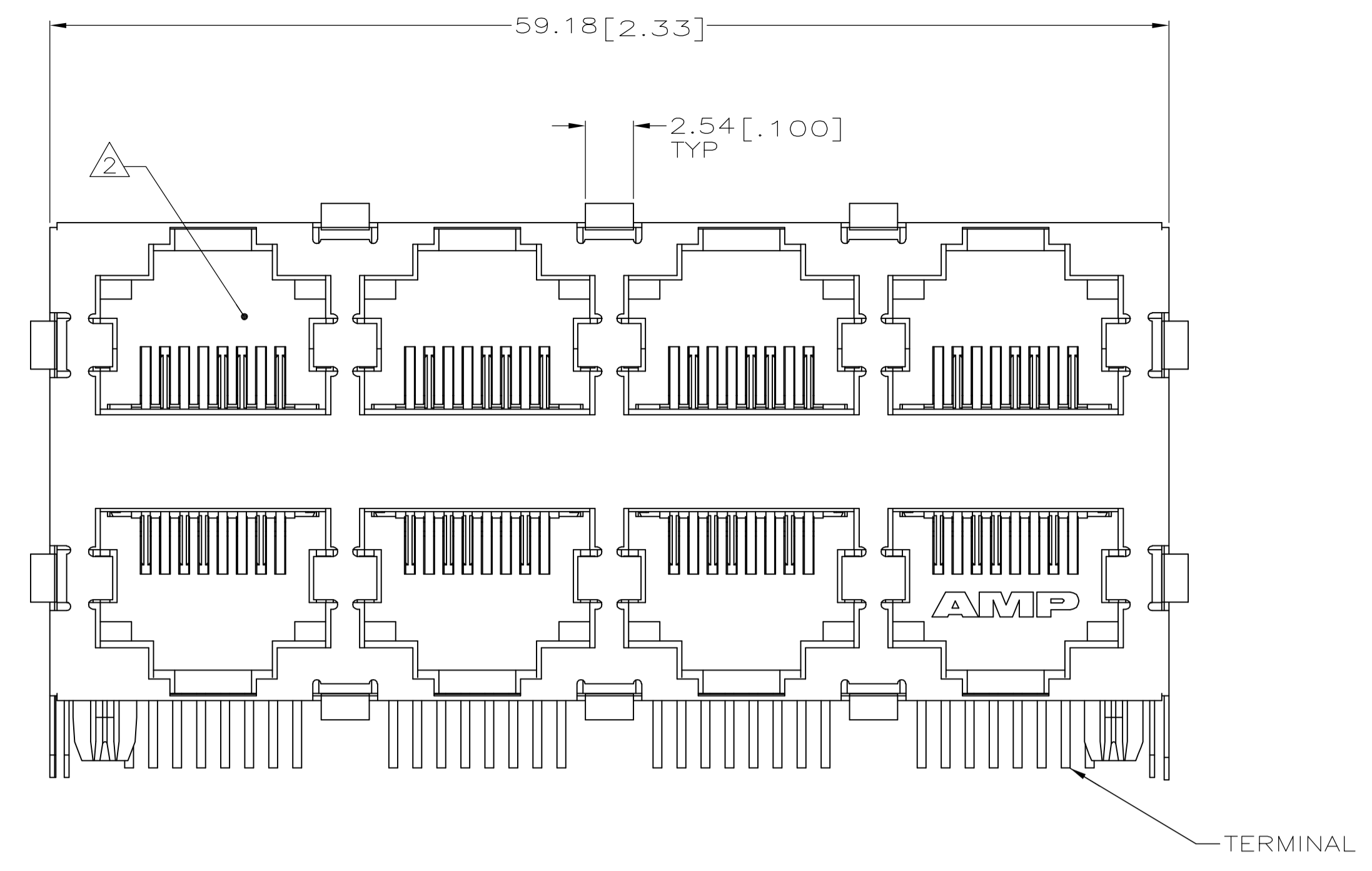
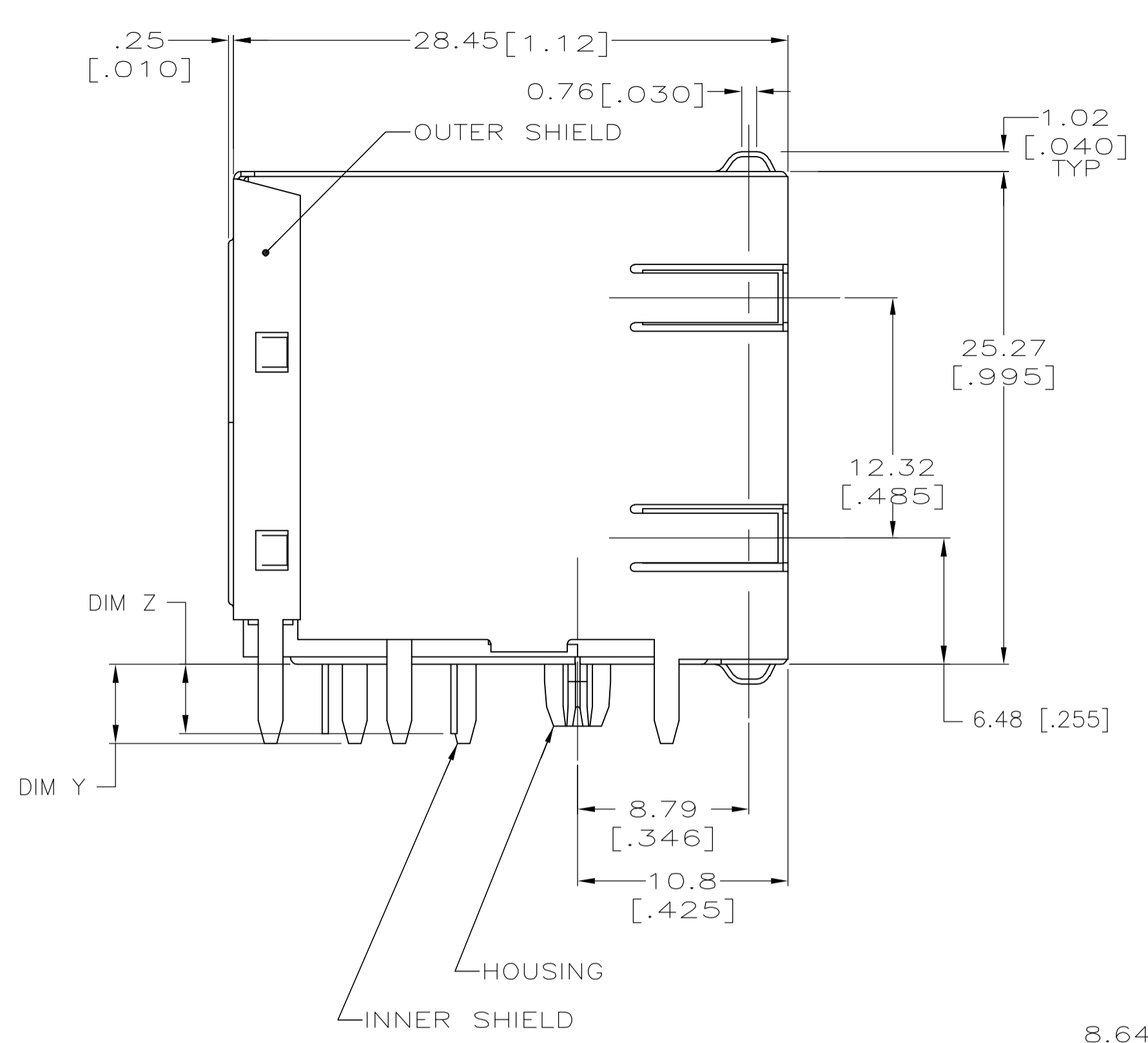


LOC		DIST		REVISONS			
AA	22	F	LTR	DESCRIPTION	DATE	DWN	APVD
		B		REV PER ECO-07-004617	14MAR07	TK	JW



PC BOARD LAYOUT VIEWED FROM COMPONENT SIDE
SCALE 2:1

THIS DRAWING IS A CONTROLLED DOCUMENT.		DIN T. SPRINKLE/L.A. MAYER 17APR2005	Tyco Electronics Corporation Harrisburg, PA 17105-3608	
DIMENSIONS: mm [INCHES]		CHK J. WESTMAN 17APR2005	APVD S. FLICKINGER 17APR2005	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		NAME: STACKED MODULAR JACK ASSEMBLY, 2x4 PORT, 8 POSITION, SHIELDED, PANEL GROUND, CATEGORY 5		
0 PLC ± -	1 PLC ± -	PRODUCT SPEC	108-1163-2	SIZE: A1
2 PLC ± 0.25[.01]	3 PLC ± 0.13[.005]	APPLICATION SPEC	114-2048	SCALE: 4:1
4 PLC ± -	ANGLES ± -	WEIGHT	00779	SHEET 1 OF 1
MATERIAL SEE NOTE 1	FINISH SEE NOTE 1	RESTRICTED TO	5569262	REV B
CUSTOMER DRAWING		SCALE 4:1 SHEET 1 OF 1 REV B		